

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	224	du.xa.	USPAT	OR	OFF	2005/09/21 13:52
S2	245	(thuan near2 (du su)).xa,xp.	USPAT	OR	OFF	2006/02/15 18:00
S4	218	(thuan near2 (du su)).xa.	USPAT	OR	OFF	2005/09/21 13:59
S5	2	("6314049" "20040034491").pn.	US-PGPUB; USPAT	OR	OFF	2006/02/15 13:21
S6	50	("6044474" "4455654" "4930101" "5925136" "5978285" "6026031" "4622690" "4627090" "4785416" "4819164" "4907225" "4916621" "4987529" "5388245" "5408639" "5481690" "5488631" "5530965" "5557783" "5607458" "5640358" "5642487" "5706407" "5748977" "5752045" "5792201" "5901301" "5926648" "5951702" "6112284" "6129633" "4328558" "4334268" "4334307" "4348929" "4367525" "4381552" "4383302" "4396938" "4398221" "4400783" "4404680" "4458308" "4459617" "4462594" "4467420" "4484310" "4488290" "4497031" "4500974").pn.	USPAT	OR	OFF	2006/02/15 15:15
S7	126	synchroniz\$5 same ((memory \$2ram) near3 latency)	USPAT	OR	OFF	2006/02/15 16:42
S8	2095	synchroniz\$5 same ((doubl\$3 mutiply\$3 multipli\$2) near2 (clock frequency speed))	USPAT	OR	OFF	2006/02/16 15:00
S9	8	S7 and S8	USPAT	OR	OFF	2006/02/15 15:39
S10	429	synchroniz\$5 same (double near2 (clock frequency))	USPAT	OR	OFF	2006/02/15 15:55
S11	123	synchroniz\$5 same ((double near2 (clock frequency)) with output\$4)	USPAT	OR	OFF	2006/02/15 16:37
S12	21	suspen\$5 near2 clock near3 period	USPAT	OR	OFF	2006/02/15 16:38
S13	3158	synchroniz\$5 with (memory \$2ram) with (\$4processor cpu)	USPAT	OR	OFF	2006/06/29 18:38
S14	2050	synchroniz\$5 with (memory \$2ram) with (\$4processor)	USPAT	OR	OFF	2006/02/15 16:44
S15	2	"clock suspension function"	USPAT	OR	OFF	2006/02/15 16:45
S16	12	S8 same S14	USPAT	OR	OFF	2006/02/15 16:45
S17	267	(thuan near2 (du su)).xa,xp.	USPAT	OR	OFF	2006/02/15 18:00
S18	248	(wait\$3 halt\$3 suspen\$4) near5 (clock near2 period)	USPAT	OR	OFF	2006/02/16 13:29
S19	9	S18 and 713/600	USPAT	OR	OFF	2006/02/16 13:29

EAST Search History

S20	100	ram with processor with synchroniz\$4	USPAT	OR	OFF	2006/02/16 14:34
S21	176	(synchroniz\$5 same ((doubl\$3 mutiply\$3 multipli\$2) near2 (clock frequency speed))).clm.	USPAT	OR	OFF	2006/02/16 15:08
S22	100	ram with processor with synchroniz\$4	USPAT	OR	OFF	2006/02/16 15:09
S23	176	(synchroniz\$5 same ((doubl\$3 mutiply\$3 multipli\$2) near2 (clock frequency speed))).clm.	USPAT	OR	OFF	2006/02/16 15:16
S24	1	S22 and S23	USPAT	OR	OFF	2006/02/16 15:15
S25	8	(synchroniz\$5 same ((doubl\$3 mutiply\$3 multipli\$2) near2 (clock frequency speed))).clm. and "713"/\$.ccls.	USPAT	OR	OFF	2006/02/16 16:30
S26	19070	motorola.as.	USPAT	OR	OFF	2006/02/16 16:31
S27	0	S26 and "sram interface"	USPAT	OR	OFF	2006/02/16 16:40
S28	0	(ram with mpc with interface) and motorola.as.	USPAT	OR	OFF	2006/02/16 16:43
S29	1416	synchroniz\$5 with (memory \$2ram) with (\$4processor cpu) with control\$4	USPAT	OR	OFF	2006/06/29 18:39
S30	377	synchroniz\$5 with (memory \$2ram) with (\$4processor cpu) with control\$4 with clock	USPAT	OR	OFF	2006/06/29 18:40
S31	8	synchroniz\$5 with (memory \$2ram) with (\$4processor cpu) with control\$4 with "second clock"	USPAT	OR	OFF	2006/06/29 18:42
S32	66	(memory \$2ram) with (\$4processor cpu) with control\$4 with "second clock"	USPAT	OR	OFF	2006/06/29 19:59
S33	58	S32 not S31	USPAT	OR	OFF	2006/06/29 18:43
S34	15	S32 and "713"/\$.ccls.	USPAT	OR	OFF	2006/06/29 18:43
S35	141	(memory \$2ram) near2 clock with (\$4processor cpu) near2 clock with control\$4	USPAT	OR	OFF	2006/06/29 20:00
S36	7	(memory \$2ram) near2 clock with (\$4processor cpu) near2 clock with "control signal"	USPAT	OR	OFF	2006/06/29 20:11
S37	1	"619501".ap.	US-PGPUB	OR	OFF	2006/06/29 20:11